

Notice of References Cited	Application/Control No. 10/665,263		Applicant(s)/Patent Under Reexamination KOGGE, PETER	
	Examiner Kimberly N. McLean-Mayo		Art Unit 2187	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,343,346	01-2002	Olnowich, Howard Thomas	711/142
*	B	US-6,266,745	07-2001	de Backer et al.	711/147
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Sterling et al., Gilgamesh: A Multithreaded Processor-In-Memory Architecture for Petaflops Computing, IEEE, 2000
	V	Kogge et al., Implications of a PIM Architectural Model for MPI, CLuster2003 Convention, 12,2003
	W	Holmes et al., Processing in Memory: The Terasys Massively Parallel PIM Array, IEEE 1995
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.